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10/24/02
JC#1082

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10053555	FILING DATE 01/24/2002	CLASS 174	SUBCLASS 2831	GAU 2831	EXAMINER Dinh
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**APPLICANTS: Ookawa Tadao; Ohwaki Yasuhito; Omote Toshihiko;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN P. 2001-017864 01/26/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO Q68175
Verified and Acknowledged Examiners's initials			
TITLE : Junction flexible wiring circuit board			

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs.Drwg.
TERMINAL DISCLAIMER		Print Fig.		
		Primary Examiner		
		PREPARED FOR ISSUE		
		Application Examiner		
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